1. Open J-Flash ARM Offene J-Flash-ARM



2. Choose options, then click project settings...Alt-F7

Optionen auswählen, klicken Sie dann auf Projekt-Einstellungen ... Alt-F7

FIRSH	SEGGER J-F	lash ARM	V4.24c - [C	: Program Fil	es\SI	GGI	R\JL	.ink/	RM_	V42	4c\Ľ	lefau	lt.jf	ilash]								
File	e Edit View	Target O	ptions Windo	w Help																			
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	Connection	USB [Dev	rice 0]			_	1	_															
T	arget interface	JTAG	-	Address	Ø	1	2	3	4	5	6	7	8	9	A	В	С	D	Ε	F	ASCII		-
				00000	18	FØ	9F	E5	18	FØ	9F	E5	18	FØ	9F	E5	18	FØ	9F	E5		 	

3. General shows as the picture:

General Target Interface CPU	Flash Production J-Flash-AFM is a software for J-Link AFM, It requires a licence, which can be obtained from SEGGER (www.segger.com). This software is capable of programming the flash memory of several AFM micros, as well as external Flash connected to AFM cores. Connection to J-Link	3101 7 E5 B9 00 B9 EA EA E2 E3	8 18 20 44 50 FE 50 D7 00	(top) 9 F0 F1 00 FF 00 F0 F0	A 9F 1F 00 00 FF 9F 21 A0	. bin B E5 E5 00 00 EA E5 E3 E1	C 18 18 48 54 FE DB 00 20	D F0 F0 00 FF F0 D0	E 9F 9F 00 00 FF 21 A0 40	F E5 E5 ØØ EA E3 E1 E2	ASCII Pn XeDH L(n.PT Pt et	
	C USB SN 0 Select	E1 E2 EB 00 E8	20 10 04 00	00 F0 00 00	40 21 9F 00 8A	E2 E3 E5 EB E0	D3 00 10 F1 00	FØ DØ FF 2E BØ	21 AØ 2F ØØ 8B	E3 E1 E1 EB EØ		
User interface mode Engineering (More options, Simplified (Less options, by	typically used for setup) nically used for production)	E1 E2 00 28 28 E3	00 01 64 FB 00 00	.10 00 ED FF 40 40	00 13 00 FF 90 B0	1A E3 00 8A 45 E3	E9 03 10 82 00 00	2E FØ 20 2E 40 50	00 47 52 80 81 80	EB 10 E2 E1 45 E3	.pJZ. 0G. /4dR. x(x(0(0(.e.e.e.e. /.0eP.	
inkAHM.dll V4.24c (DLL compiled ding flash device list (C:VProgram F st of flash devices read successfull ding MCU device list (C:VProgram I	OK Cancel Apply Mar 4 2011 2004:34]											

4. Click "CPU"

Klicken Sie auf "CPU"

• D	revice NXP LP ore Little end Clock s © Aut C	c2478 dian 💌 speed o detection 0 Hz	• •	 Check core ID ID 4F1F0F0F Use target RAM (faster) Addr 40000000 64 KB
# 0	Action Reset	Value0 0	Value1 0 ms	Comment Reset and halt target

5. Set as the picture, at last click ok:

Als das Bild endlich auf OK:

Device NXP L Core Little e Clock Cock Coc	PC2478 PC2478 speed to detection 0 H2		Check core ID ID 4F1F0F0F Use target RAM (faster) Addr 40000000 64 KB	7 8 E5 10 B9 20 00 4 B9 50 EA F1	Sktop Wess. 9 A 8 F0 9F 0 F1 1F 4 00 00 0 00 00 E FF FF
Use following init s # Action 0 Reset	equence: Value0 0	Value1 0 ms	Comment Reset and halt target	EA 51 E2 D' E3 01 E1 20 E2 10 E8 04 E8 04 E8 04 E8 04 E2 05 00 64 28 FJ	0 0 9 9 1 7 F0 21 1 0 00 40 1 0 00 40 1 0 00 21 1 0 00 9 1 0 00 9 1 0 00 00 1 0 00 00 1 0 00 00 1 0 00 13 1 1 00 13 1 4 ED 00 1
	nsert Delet		Up Down	28 00 E3 00	04090 040B0

6. Click options again, choose also project settings... Alt-F7Klicken Sie auf Optionen erneut, wählen Sie auch die Projekteinstellungen ... Alt-F7

n ar	M V4.24c - [C: Program Fi	les weggen ut mkara
get (Options Window Help	
ult	Glogal settings Alt-F7	ments and Settings\
alue	Address: 0x0) x1 x2 x4

7. Click "Target Interface" and set as the picture, at last click OK:

Klicken Sie auf "Target Interface", und wie das Bild gesetzt, endlich klicken Sie auf OK:

General Target Interface CPU F	lash Production		ator\De
JTAG JTAG speed before init Auto selection Adaptive clocking	JTAG speed aft • Auto selecti • Adaptive cl	ter init ion ocking	7 (E5 1 B9 2 00 4
JTAG scan chain information Auto detection Simple configuration Position 0 is clo	n O V IRPre O sest to TDO. Sum of IR to TDO. I devices it	Verify Detect	B9 5 EA F EA 5 E2 D E3 0 E1 2 E2 1
C Detailed configuration TD0 TD0 Add Insert	Delete Edit	IRLen Up Down	EB 0 00 0 E8 0 E1 0 E2 0 00 6 28 F 28 0 E3 0
\sim		Caricel Apply	j

8. Close the "kess.bin" as the picture:

Schließen Sie den "kess.bin", wie das Bild:

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Endian	Little	00060	00	DØ	AØ	E1	20	00	40	E2	D7	FØ	21	E3	00	DØ	AØ	E1	e !		
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9. Open data file as kess

Offene Datendatei als kess

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Desktop Documents y Computer	☆ 一键还原精灵 및 J-Flash ARM ■ Conguage Options	

10. Click OK Klicken Sie auf OK

Enter start address	2
Start address	OK Cancel

11. Click "Target", then choose "auto"

Klicken Sie auf "Ziel", und wählen Sie "auto"

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SEGGER J-Flash ARM V4.24c - [C:\Program Files\SEGGER\JLinkARM_V42

12 J-Flashing automatically

J-Blinken automatisch

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